

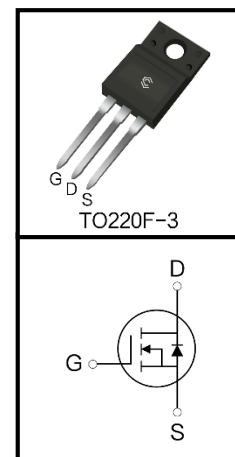
500V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information

Device	Package	Marking
CS20N50FF	TO220F-3	CS20N50FF

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS} = 0V$)	V_{DSS}	500	V
Continuous Drain Current	I_D	20	A
Pulsed Drain Current (note1)	I_{DM}	80	A
Gate-Source Voltage	V_{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E_{AS}	649.8	mJ
Avalanche Current (note1)	I_{AS}	11.4	A
Repetitive Avalanche Energy (note1)	E_{AR}	2.60	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	85	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	$-55 \sim +150$	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	1.47	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	100	

Specifications $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain–Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	500	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 500V, V _{GS} = 0V, T _J = 25°C	--	--	1	μ A
Gate–Source Leakage	I _{GSS}	V _{GS} = ± 30V	--	--	± 100	nA
Gate–Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	4.0	V
Drain–SourceOn–Resistance(Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 10A	--	0.21	0.27	Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 200kHz	--	2753	--	pF
Output Capacitance	C _{oss}		--	274	--	
Reverse Transfer Capacitance	C _{rss}		--	10	--	
Gate Resistance	R _g	V _{GS} = 0V,f = 1.0MHz	--	1.4	--	Ω
Total Gate Charge	Q _g	V _{DD} = 400V, I _D = 20A, V _{GS} = 10V	--	51	--	nC
Gate–Source Charge	Q _{gs}		--	12	--	
Gate–Drain Charge	Q _{gd}		--	17.5	--	
Turn–on Delay Time	t _{d(on)}	V _{DD} =250V, I _D =20A, R _G =25Ω	--	55.5	--	ns
Turn–on Rise Time	t _r		--	32.7	--	
Turn–off Delay Time	t _{d(off)}		--	226	--	
Turn–off Fall Time	t _f		--	58.5	--	
Drain–Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25 °C	--	--	20	A
Pulsed Diode Forward Current	I _{SM}		--	--	80	
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 10A, V _{GS} = 0V	--	--	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} = 0V, I _S = 20A, diF/dt = 100A/ μ s	--	318.6	--	ns
Reverse Recovery Charge	Q _{rr}		--	4.5	--	μ C

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L=10mH, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

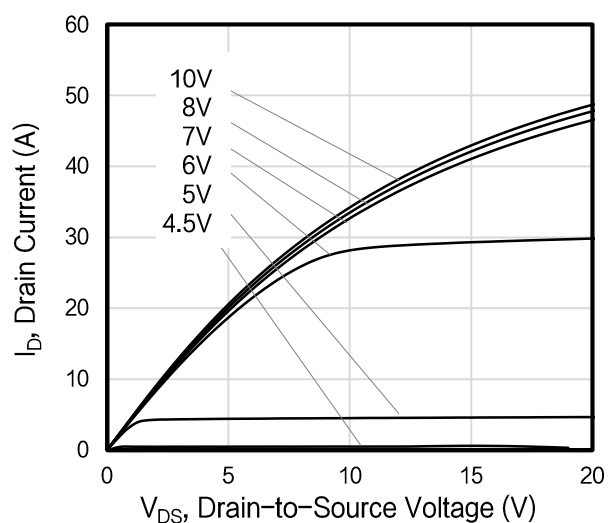


Figure 2. Body Diode Forward Voltage

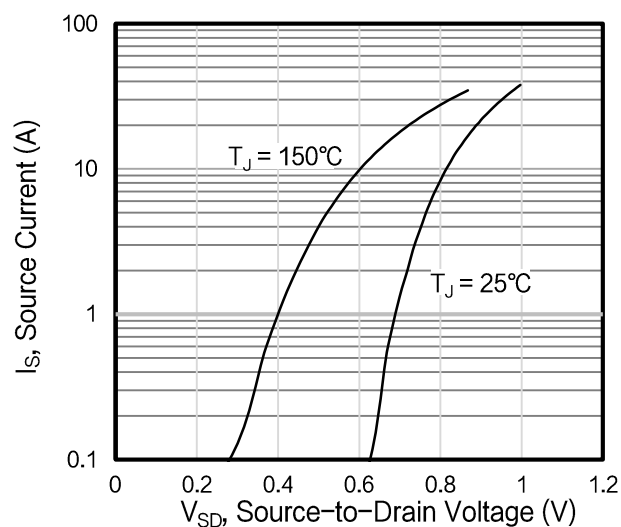


Figure 3. Drain Current vs. Temperature

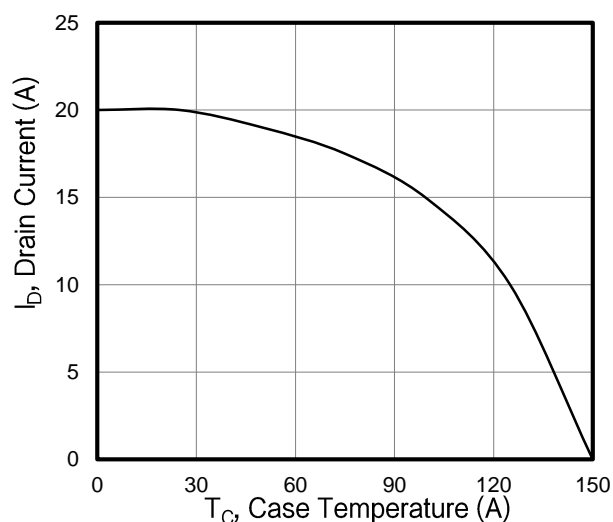


Figure 4. BV_{DSS} Variation vs. Temperature

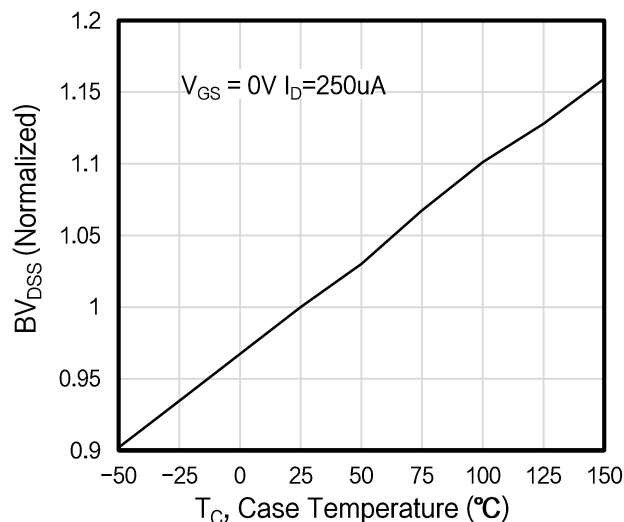


Figure 5. Transfer Characteristics

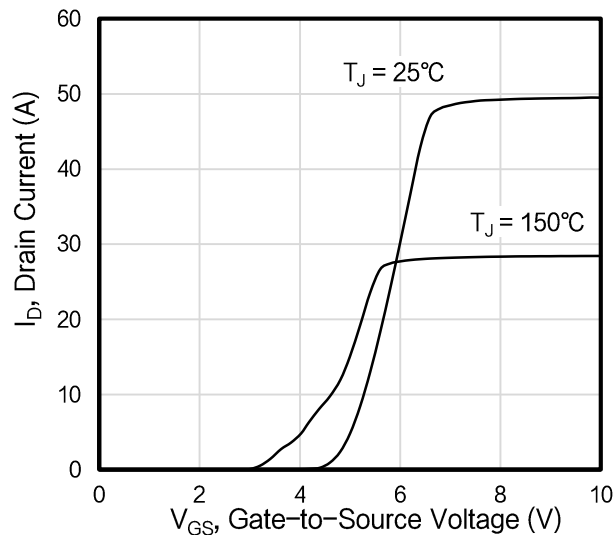
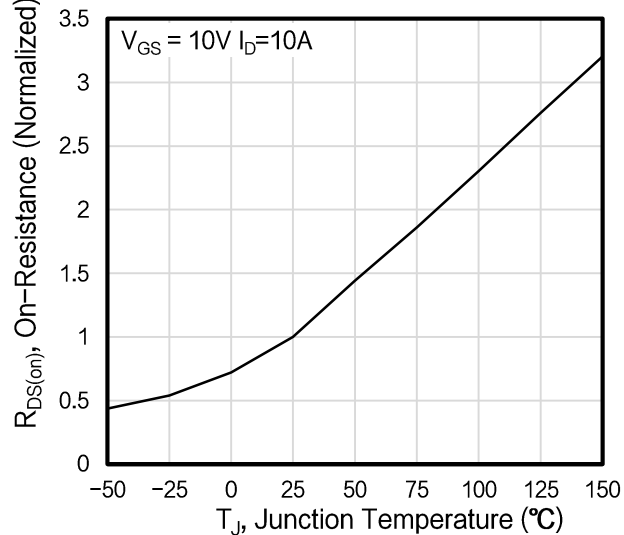


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Capacitance

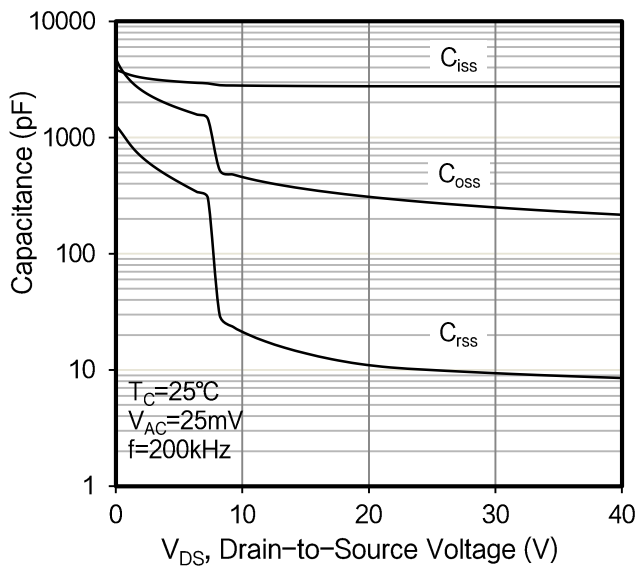


Figure 8. Gate Charge

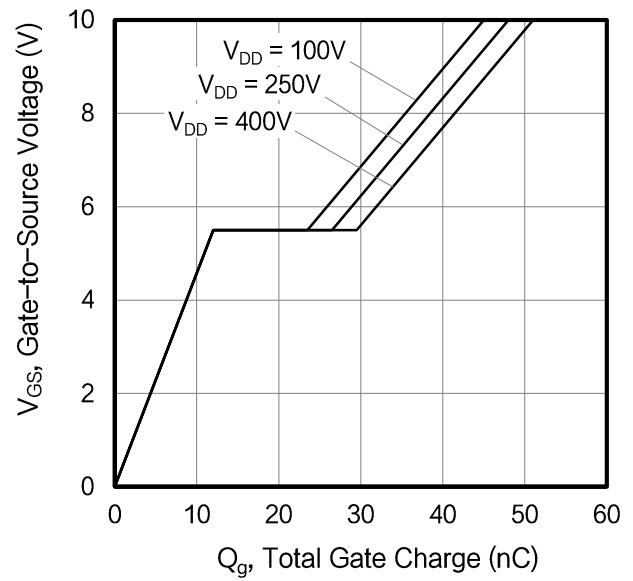


Figure 9. Transient Thermal Impedance

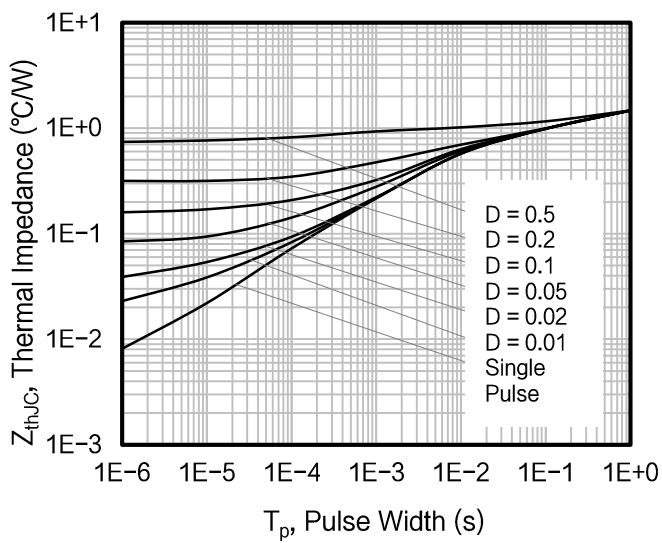


Figure A: Gate Charge Test Circuit and Waveform

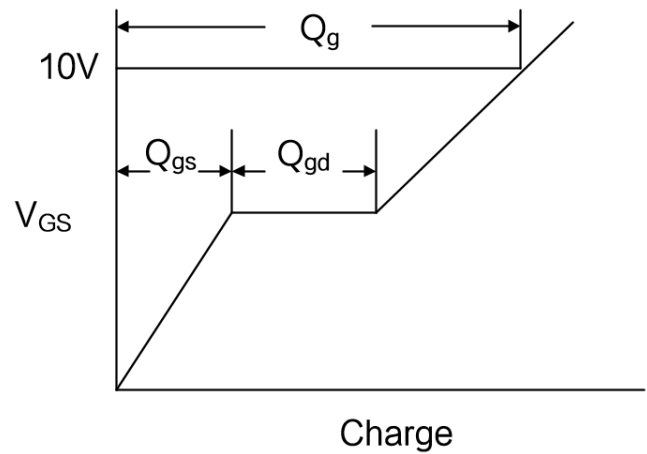
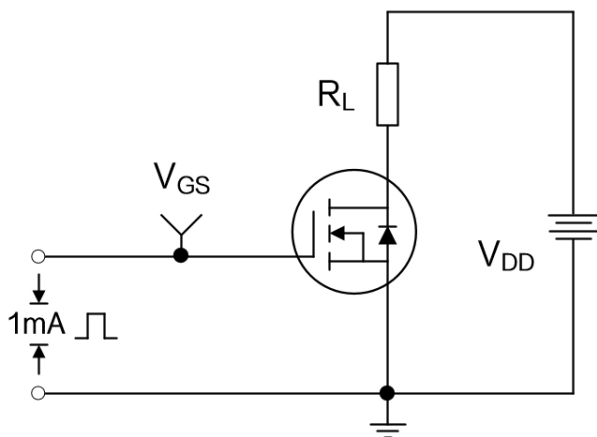


Figure B: Resistive Switching Test Circuit and Waveform

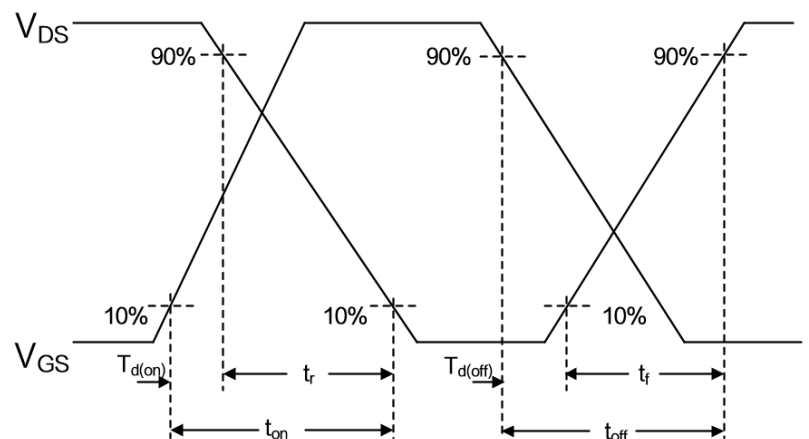
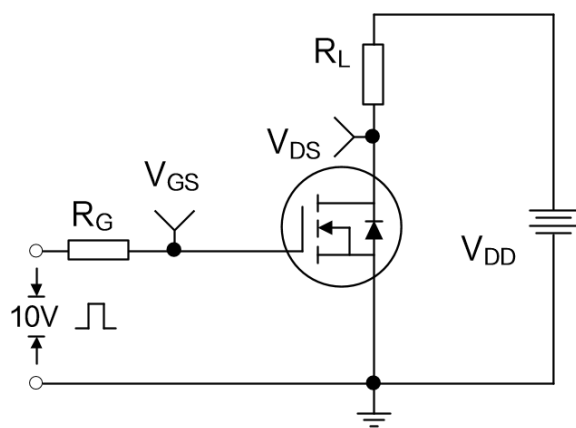
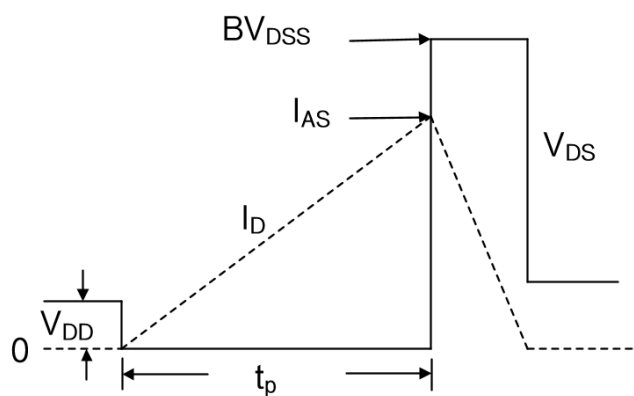
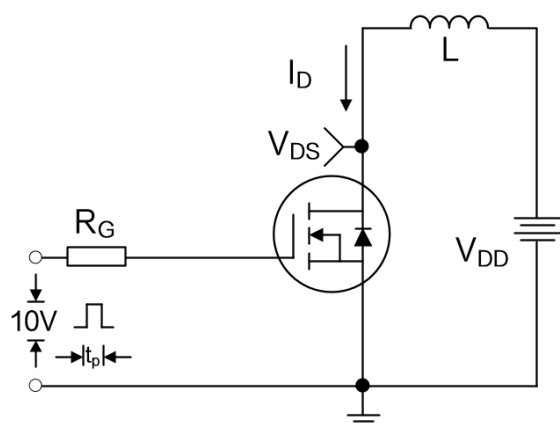
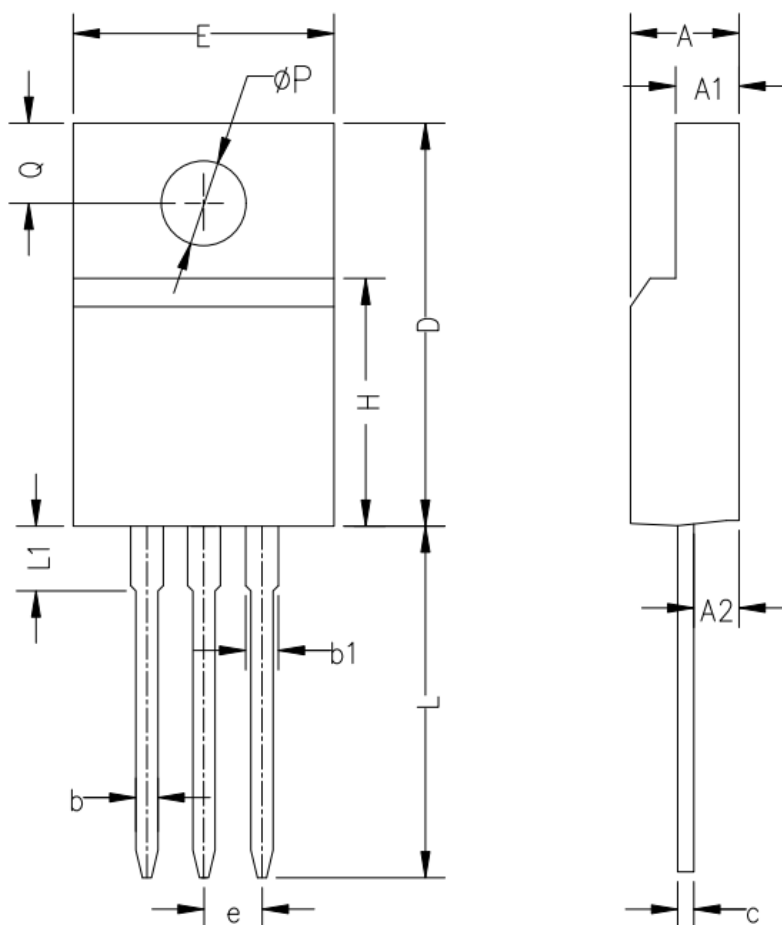


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



TO220F-3



SYMBOLS	MILLIMETERS	
	MIN	MAX
A	9.70	10.30
B	15.50	16.10
B1	8.99	9.39
C	4.40	4.80
C1	2.15	2.55
D	2.50	2.90
E	0.70	0.90
F	0.40	0.60
G	1.12	1.42
H	3.40	3.80
L	12.60	13.60
N	2.34	2.74
Q	3.15	3.55
ϕP	3.00	3.30

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